

In re Patent Application of:
COFLER
Serial No. 10/083,629
Filing Date: February 26, 2002

In the Specification:

Please replace the paragraph beginning at page 7, line 11, with the following rewritten paragraph:

Among the instructions issued to the processing units ~~feature~~ are instructions called guarded instructions allowing higher performance writing of the program code. The use of guarded instructions in a processor is already known to those skilled in the art, for example, reference is directed to European Patent Application No. 1,050,803. The principle of guarded instructions is reiterated briefly here, however, reference is directed to the abovementioned European patent application for further details.

Please replace the paragraph beginning at page 13, line 36, with the following rewritten paragraph:

According to one implementation of the invention, the first control means include a first read counter incremented every time an instruction is extracted from the first memory, a first write counter incremented every time an instruction is stored in the first memory, and a set of first individual registers associated respectively with the set of registers capable of being loaded by memory stored ~~data,~~ data.

Please replace the paragraph beginning at page 22, line 13, with the following rewritten paragraph:

The central unit which receives such an ~~instructions~~ instruction puts it into an instruction for dividing the

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memory stored data, intended for the addressing unit AU, and into a loading instruction intended for the unit DU. The deriving instruction will allow the unit AU, via the memory controller DMC, to read the data stored in memory at the address supplied by the pointer Py and to transfer this memory stored data to the memory LDQ.